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Scanning Kelvin Probe Microscopy investigation of the contact resistances and charge mobility in n-type PDIF-CN₂ thin-film transistors



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ABSTRACT

In this paper, Scanning Kelvin Probe Microscopy (SKPM) was applied in combination with standard currentvoltage measurements to assess the contact resistance (R_C) effects and to estimate the contact-free charge carrier mobility in n-type transistors based on evaporated films of PDIF-CN₂. This analysis was carried out in particular as a function of the applied polarization voltages. The separate contributions to the R_C phenomenon coming from the injecting (source) and extracting (drain) electrodes were investigated with the possibility to separately analyze the specific electrical behaviour of these two regions. In agreement with previous results, the experimental data here discussed show that the devices under analysis are not simply injected limited. Voltage drops at both the injecting and extracting contacts must be taken into consideration. However, it is worth to mention that for the bottom-contact devices here investigated, a minimum R_C of 5.5 k Ω cm was found in linear regime. These values are significantly lower (i.e. by a factor 5×) than those previously reported for other perylene dimitide thin-film devices with bottom-contact configuration.

1. Introduction

In the past three decades, scientific attention has been actively focused on the possible technological breakthrough of organic electronics and on the effective application of its paradigms, consisting in the production of flexible, low cost and easy-to-process devices for mass consumer electronics [1]. Rapid advancements prompted the development of a wide number of organic devices such as light-emitting diodes (OLEDs) [2], solar cells [3,4], memories [5], light emitting field-effect transistors (OLETs) [6] and field-effect transistors (OFETs) applied as sensors [7] or for the development of complex circuitry [8].

In the field of OFETs, in particular, remarkable improvements have been achieved in terms of reliability and overall electrical response using innovative compounds which, nowadays, can firmly exceed the performances of amorphous silicon devices. However, within the vast family of newly synthesized organic semiconductors [9], the availability of high performing n-type materials, enabling the development of effective CMOS-like organic circuits for integrated electronic systems, has been an issue for long time [10]. In this context, perylene-3,4,9,10-tetracarboxylic acid diimide derivatives, also known as perylene diimides (PDI's) compounds, have gained great attention in the last years, thanks to their capability to exhibit good n-type charge transport and enhanced stability in ambient conditions, enabled by suitable chemical functionalization [11,12]. Within PDI family, the cvano-functionalized PDIF-CN2, characterized by the insertions of fluorinated chains (CH₂C₃F₇) on the imides' positions, has been used for the fabrication of n-type transistors based on solution-crystallized channels [13], high performing single crystals [14], highly ordered thin films deposited by Organic Molecular Beam deposition (OMBD) [15] and Supersonic Molecular Beam Deposition technique (SuMBD) [16,17]. In the best cases, mobility values largely exceeding $1 \text{ cm}^2/\text{Vs}$ have been extracted from the transfer-curves of these devices. Despite these important advances, severe bottlenecks in the electrical performances and a lack of precise knowledge of the basic principles ruling the charge injection and transport in organic semiconductors are still an open issue [18], especially for thin film transistors (OTFTs). The main drawbacks are to date the strong charge trapping phenomena induced by both chemical defects and structural inhomogeneity [19-21], as well as the presence of intrinsic contact resistances $(R_{\rm C})$ at the injecting and extracting electrodes. Because of the contact resistances, a part of the applied voltage between the drain and source (V_{ds}) electrodes is lost across the organic/metal interfaces, downgrading the general performances of the device (in particular in the low V_{ds} region) and hindering considerably their miniaturization capabilities towards the sub-

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http://dx.doi.org/10.1016/j.orgel.2017.10.021 Received 1 August 2017; Received in revised form 6 October 2017; Accepted 17 October 2017 Available online 20 October 2017 1566-1199/ © 2017 Elsevier B.V. All rights reserved. micrometric scale [22]. The physical reasons behind this phenomenon are mainly relatable to the intrinsic characteristics of the heterojunctions forming between the polycrystalline organic films and the metallic electrodes. Firstly, the energetic mismatch between the Fermi level of the metal electrode (E_F) and the LUMO (HOMO) level of the n-type (ptype) gives rise to the formation of interfacial potential barriers which charge carriers must overcome when injected in or extracted from the device active channel. Moreover, in particular for some specific device configurations, morphological aspects associated to the defective growth of the semiconducting film in the vicinity of the electrodes are able to play a dominant role in the consistent enhancement of R_C values [23].

A powerful approach to characterizing the contact resistance effect in OTFTs is to image the potential distribution along the conducting channel during the device operation by Scanning Kelvin Probe Microscopy (SKPM). By using this technique, indeed, contact resistances are directly measured through the acquisition of the potential difference building up between the tip of an AFM cantilever and the sample. In recent years, SKPM has been widely used in determining surface voltage characteristics and contact effects in pentacene [24], P3HT [25] and TPD:PS [26] organic devices, exploiting the advantage to estimate separately the contributions of source and drain electrodes, in contrast with alternative techniques such as the transmission line method [27] and gated four-probe configuration [28].

In this paper, we present a systematic analysis by means of SKPM of the contact resistance phenomena affecting the response of bottomcontact bottom-gate PDIF-CN2 OTFTs. The investigation here reported is as a natural follow-up of a previous work by Chiarella et al. [29] on a parent perylene diimide derivative (PDI8-CN₂), where, for thermally evaporated films, R_C values were estimated as a function of the semiconductor layer thickness and in view of the gold electrode functionalization by an aromatic thiol self-assembled monolayer. Similarly to what was found in that previous study, we demonstrate that the $R_{\rm C}$ values in the n-type OTFTs under consideration are strongly dependent on the applied V_{ds} and weakly dependent on V_{gs} . Scanned probe measurements of the local electrostatic potential allowed us to accurately infer the charge carrier mobility in spite of large potential drops present at the injecting and extracting contacts. Both the contact resistance and the charge mobility were studied as function of temperature as well. Finally, SKPM measurements were used to investigate the peculiar behaviours of charge injection and extraction processes occurring in the electrode/organic regions through the related current-electric field (I-E) relations.

2. Materials and methods

A standard bottom-contact bottom-gate architecture has been chosen as test device in this work. PDIF-CN2 films were deposited on the top of square-shaped (1 cm²) multi-layered structures consisting of a 500 μ m thick highly-doped silicon substrate (n⁺⁺), acting as distributed gate electrode, 200 nm of thermally grown SiO₂ layer as insulating barrier and photolithographically-processed 170 nm thick interdigitated Au electrodes defining active channels with length $L = 20 \,\mu\text{m}$ and width $W = 11 \,\text{mm} (W/L = 550)$. The electrode profile is characterized on both sides by a transition region with an initial gradual sloping which is then interrupted abruptly by a down step of about 80 nm (see the lower panel of Fig. 1(c)). Prior the organic film deposition, the dielectric surface was treated with hexamethyldisilazane (HMDS) through an accurate protocol described in detail in Ref. [30]. It is important to stress that HMDS action limits primarily the intrusion of water molecules at the organic/dielectric interface improving consequently the device performances by reducing bias-stress effects both in accumulation and depletion operation modes [21]. Moreover, the SiO₂ surface functionalization by the HMDS self-assembling monolayer enhances the overall morphological quality of the PDIF-CN₂ films [15].

In this work, 25 nm thick PDIF-CN₂ films were grown by Organic Molecular Beam Deposition (OMBD) technique in high vacuum conditions ($P = 10^{-7}$ mbar). The organic powder (Polyera ActivInk N1100) was evaporated by means of a Knudsen cell kept at 220 °C, providing a deposition rate of 0.3 nm/min. The substrate was kept at about 105 °C in order to optimize the film growth mode and guarantee an enhanced electrical response in terms of field-effect mobility [31].

A preliminary electrical characterization of the fabricated PDIF-CN₂ OTFT was performed both in air and in vacuum by using a Janis Probe Station connected to a Keythley 2612A Dual Channel source-meter. In first approximation, it is possible to apply the standard MOSFET theory [32] to model the OTFT response and to extract the field-effect mobility values from the transfer characteristics in linear regime ($|V_{ds}| \ll |V_{gs} - V_{th}|$):

$$\mu_{lin} = \frac{\partial I_{ds}}{\partial V_{gs}} \frac{L}{W C_{ox}} \frac{1}{V_{ds}}$$
(1)

and in the saturation regime ($|V_{ds}| \ge |V_{gs} - V_{th}|$):

$$\mu_{\text{sat}} = \left(\frac{\partial \sqrt{I_{\text{ds}}}}{\partial V_{\text{gs}}}\right)^2 \frac{2L}{WC_{\text{ox}}}$$
(2)

where *L* and *W* indicate the channel length and width, respectively, C_{ox} is the oxide capacitance per unit of area, I_{ds} and V_{ds} are the drain-source current and the drain-source applied voltage, respectively. V_{gs} indicates the gate-source voltage while V_{th} is the threshold voltage. It should be pointed out, however, that although the gradual channel approximation can be considered satisfied in our system (i.e. channel length $L = 20 \,\mu\text{m}$ is much larger than oxide thickness $d = 200 \,\text{nm}$), standard MOSFET theory does not take into account the possible trapping effect at the oxide/organic semiconductor interface [33] or the occurrence of non-ohmic processes at the source and drain contacts. For these reasons, careful attention must be paid when interpreting characteristic parameters of OTFTs, such as field-effect mobility [34] or threshold voltage [35], by using eqs. (1) and (2).

In this study, Amplitude Modulation Scanning Kelvin Probe Microscopy (AM-SKPM) measurements were performed by a XE-100 Park atomic force microscope equipped with Cr/Au-coated conducting cantilevers (NSC14 Cr/Au MicroMask^(tm)). SKPM profiles were acquired in Argon atmosphere, in dual frequency mode (i.e. acquiring simultaneously both the height profile and the potential profile of the OTFT active channel) and biasing the drain and gate electrodes. At the same time, the current was monitored in order to control the device stability. Variable temperature measurements were obtained using a heated scanner in the range 300–380 K. Further details on the acquisition methods and parameters are reported in the Supplementary Material.

3. Results

3.1. Contact resistance evaluation

PDIF-CN₂ OTFTs were firstly tested by standard current–voltage measurements to obtain the output and transfer characteristics. The recorded electrical performances in vacuum were found to be very close to the state-of-the-art results reported for this type of device [11], with maximum mobility values of ~0.5 cm²/V·s in saturation regime. Similarly to what usually found for most OTFT configurations and even excluding the contact resistance (R_c) effects (see the discussion in the following), mobility in saturation regime is more than doubled in comparison with the highest values ($\mu_{max} \sim 0.2 \text{ cm}^2/V\cdot\text{s}$) extracted in linear regime. On the other hand, the threshold voltages (V_{th}) for these devices are negative and range between -10 and 0 V, being this feature typically ascribed to the large electronic affinity of the Perylene diimide compounds [21].

Representative output curves acquired in vacuum are shown in Fig. 1(a) and (b) where, for V_{ds} approaching to zero, the influence of the



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0 V $< V_{\rm gs} <$ 40 V. (**b**) Output curves for low drain-source field (0 V $< V_{ds} < 5$ V). (c) Surface potentials for different applied V_{ds} and $V_{gs} = 30$ V, acquired at room temperature and in Argon saturated atmosphere (upper panel). The correspondent topography of the channel is showed in the lower panel where Source electrode is located on the left and Drain electrode on the right. Surface potential profiles in (c) are shifted (and scaled for V_{ds} = 0.5 and 0.2 V) for clarity. Major ticks spacing along the y-axis in the upper panel is of 1 V.

contact resistance phenomenon gives rise to the generally-observed diode-like (S-shape) behaviour. Here, the channel (I_{ds}) current deviates considerably from the linear trend predicted by the standard MOSFET model, revealing the presence of the parasitic contact effects which tend to hinder the charge injection and/or extraction processes at the PDIF- CN_2 /gold interfaces [36]. As a result, the scaling behaviour of the transfer curves is highly dependent on the applied V_{ds} voltages and the effective field-effect mobility (μ_{FET}) in linear regime is strongly decreasing at reducing V_{ds} . In order to quantify the R_{C} contribution to the overall response of the device and gain eventually insights about the charge transfer mechanisms operating at metal/organic interfaces, surface voltage profiles across the active channel were imaged by means of SKPM during the device operation. Typical potential profiles, acquired at room temperature while keeping the gate bias (V_{gs}) fixed at 30 V and varying the drain-source (V_{ds}) voltage between 0.2 V and 5 V, are reported in Fig. 1(c). For any fixed bias condition, multiple lines were recorded on the same section of the active channel and then averaged in order to minimize the instrumental noise (~ 20 mV). Nevertheless, it is worth to mention that in AM-SKPM measurements, a systematic underestimation of the measured surface voltage could occur due long-range capacitive coupling of the cantilever [37]. Surface voltage profiles in Fig. 1(c) reveal that the active channel of the investigated OTFT can be electrically described as the series of three different resistive regions. The two voltage drops at the metal/organic interfaces manifest the presence of contact effects, while the central part responds linearly to the externally applied V_{ds} accordingly to the MOSFET model. Moreover, the voltage drops $(\Delta V_{d}, \Delta V_{s})$ in correspondence of the drain and source electrodes are dependent on the applied $V_{\rm ds}$ and their percentage weight is considerably enhanced (see below) while reducing V_{ds} .

After acquiring the voltage profiles, the contact resistance values at the source (R_s) and drain (R_d) electrodes, expressed in k Ω ·cm in order to facilitate the comparison with other data reported in literature, were evaluated by simply dividing the corresponding voltage drops (ΔV_s , $\Delta V_{\rm d}$) by the width-normalized current density $I_{\rm ds}/W$. Hence, the overall R_C contribution was achieved through the sum of these terms $(R_{\rm C} = R_{\rm s} + R_{\rm d})$. As shown in Fig. 2(a), the contact resistance values for the devices under investigation were found to display a strongly increasing behaviour when V_{ds} is decreased. In particular, for $V_{ds} \le 1$ V, the Drain appears to be more affected by the contact resistance phenomenon and R_d is quite larger that R_s. For the set of data reported in Fig. 2(a), for example, at $V_{ds} = 0.2$ V, R_d is about 34 k Ω cm, while R_s is



Fig. 2. (a) Behaviour of the contact resistances as function of V_{ds} voltage for a fixed gatesource bias ($V_{gs} = 30$ V) and (b) as function of the gate bias for a fixed drain-source potential ($V_{\rm ds} = 5$ V).

slightly larger than 20 k Ω cm. Conversely, for $V_{\rm ds}$ values ranging between 3 and 5 V, R_s and R_d exhibit comparable values of few k Ω ·cm.

Another general picture about the contact influence on the overall device response is given in the Supplementary Material (see Fig. S1), where the ratio between $R_{\rm C}$ and the total device resistance ($R_{\rm total}$, extracted from the output curves) is plotted as function of V_{ds} . Here, for $V_{\rm ds}$ < 2 V, the device is completely contact-limited since the majority of the applied voltage drops across the metal/organic contacts. Beyond the $V_{\rm ds}$ impact, the dependence of the contact resistances on the applied gate voltage ($V_{\rm gs}$) was also explored, as shown in Fig. 2(b), where $R_{\rm C}$ values estimated for $V_{ds} = 5$ V and as a function of V_{gs} are reported. In



Fig. 3. Statistical distribution of the voltage drops at (a) the source and (b) drain electrodes for $V_{\rm gs}$ = 30 V and $V_{\rm ds}$ = 5 V.

general, for the investigated devices, a rather weak dependence of $R_{\rm C}$ on $V_{\rm gs}$ was found, with typical changes by about 40% when $V_{\rm gs}$ goes from 10 to 40 V (namely, this gate voltage range represents the full accumulation region). As a whole, $R_{\rm C}$ is only slightly modulated by $V_{\rm gs}$ in comparison, in particular, with the theoretical channel resistance behaviour ($R_{\rm ch} \propto 1/(V_{\rm gs}-V_{\rm th})$).

In order to gain further knowledge about contact phenomena, the local "variability" of the contact resistances was investigated by analyzing various regions of the same device. In this way, we could study the statistical distribution of the voltage drops (ΔV_s , ΔV_d) at the source and drain contacts on a vast area of the OTFT active region. To this aim, $\Delta V_{\rm s}$ and $\Delta V_{\rm d}$ were mapped over 85 different locations of the device, while keeping a fixed polarization ($V_{gs} = 30$ V and $V_{ds} = 5$ V) in the linear regime. The observed voltage distributions are shown in Fig. 3(a,b), demonstrating to follow quite well typical Gaussian behaviours. From these data, in particular, the source interface appears to be much more sensitive on the statistical oscillations in comparison with the drain interface. Indeed, while an average voltage drop of 0.6 V and a standard deviation of 0.36 V were observed for the source interface, the same parameters were found to be 0.82 V and 0.20 V, respectively, for the drain electrode [38]. From the statistics on the drain and the source electrodes, assuming the current density to be homogenous along the channel width, an overall average $R_{\rm C}$ value of 4.9 \pm 1.2 k Ω cm is obtained.

3.2. Estimation of the contact-free mobility and temperature effects

The physical identification (see Fig. 1(c)) through the SKPM profiles of the central region of the device channels, where the theoretical linear behaviour is well verified, provides also a direct method to estimate the charge carrier mobility excluding any contact effect. Indeed, by simply extracting from SKPM measurements the voltage value ΔV_{ch} dropping across the central linear zone, the contact-free mobility μ_{CFree} has been



Fig. 4. Comparison between the contact-free mobility (μ_{CFree}) extracted from the SKPM voltage profiles using eq. (3) and the field-effect mobility (μ_{FET}) obtained from linear transfer curves as function of (**a**) V_{ds} for a fixed gate-source bias ($V_{gs} = +30$ V) and of (**b**) V_{gs} for a fixed drain-source bias ($V_{ds} = +5$ V).

calculated as:

$$\mu_{\rm CFree} = \frac{L_{\rm ch} I_{\rm ds}}{W C_{\rm ox}} \times \frac{1}{\Delta V_{\rm ch} (V_{\rm gs} - V_{\rm th})}$$
(3)

where L_{ch} indicates the length of the channel central region, W is the overall channel width, I_{ds} is the device current measured during the voltage profile acquisition, C_{ox} the capacitance of the oxide per unit of area and V_{th} is the threshold voltage.

As reported in Fig. 4, $\mu_{\rm CFree}$ appears to be basically independent on the drain-source voltage, in contrast with what takes place for the field-effect mobility $\mu_{\rm FET}$ obtained from the transfer-curves recorded at various $V_{\rm ds}$. Indeed, $\mu_{\rm FET}$ shows a discrepancy larger than two orders of magnitude for low $V_{\rm ds}$ (Fig. 4(a)) with an exponential decrease towards 10^{-3} cm²/V·s in correspondence of $V_{\rm ds} = 0.2$ V.

Using the aforementioned approach (see Fig. 4(b)), it was also found that μ_{CFree} tends to increase linearly with the gate voltage (V_{gs}). Such behaviour is commonly associated to the so-called Multiple Trap and Release transport model, where the predicted V_{gs} dependence of mobility is strictly dictated by the amount of charge induced at the dielectric/organic interface and progressively released from shallow traps at rising V_{gs} [39]. On the other hand, Fig. 4(b) highlights the role of the contact resistances which, counterbalancing the aforementioned growth of the contact-free mobility for high V_{gs} , make the field-effect mobility μ_{FET} apparently decreasing with the gate bias.

In order to complete the characterization of the devices under investigation, the surface potentials were measured as a function of temperature from T = 300 K to T = 350 K. At any temperature, the voltage profiles were acquired by keeping the gate potential fixed ($V_{gs} = 30$ V) and considering $V_{ds} = 0.5$ V, 3 V and 5 V. The SKPM profiles achieved in this way are plotted in Fig. S2.

Focusing our attention on the voltage drops at the source and drain electrodes, it appears that they display a mild decaying behaviour at increasing temperature which is more evident for $V_{\rm ds}$ = +5 V and +3 V (Fig. 5(a)). In these cases, going from room temperature to 350 K,



Fig. 5. (a) Voltage drops at the Source and Drain interfaces as function of the temperature. (b) Drain-source current and (c) contact resistances as function of the temperature and for different bias conditions. For all the images $V_{gs} = 30$ V while $V_{ds} = 0.5$ V, 3 V, 5 V starting from the lower panels.

the voltage drops experience a decrement of the order of 18% at both the source and drain interfaces.

On the other hand, the effect of the temperature is much more evident when referring to the charge transport of the device active channel. Indeed, by extracting the contact-free mobility dependence on temperature using this same set of data, we could observe a typical Arrhenius-like behaviour characterized by an activation energy of $\Delta_{\rm E} = 107 \pm 4$ meV. This value was achieved fitting data in Fig. S3 while neglecting the experimental point at 350 K, where thermal instabilities affecting the data reliability were observed in most measurements. It should be outlined that the activation energy here estimated is in good agreement with previously reported data for cyanated perylene diimide compounds [40]. Obviously, this thermal trend is immediately reflected into the I_{ds} currents that undergo to a rapid growth with temperature, at any polarization (Fig. 5(b)). Provided these findings which emphasize the role of the thermally-assisted transport properties of PDIF-CN₂, it is not surprising to observe that the overall trend of contact resistances as function of the temperature is dominated by the I_{ds} current enhancement, giving birth to a general decrement up to 50% for temperatures approaching 350 K (Fig. 5(c)). As a further confirmation of this occurrence, Fig. S3 shows also that the product between R_C and mobility is basically independent on the temperature [41].

4. Discussion

In qualitative terms, the phenomenology of contact resistances here observed for PDIF-CN₂ OTFT seems to agree very well with the results previously reported for PDI8-CN₂ devices having the same configuration [29]. In fact, for both cases, we have found that $R_{\rm C}$ values are strongly dependent on the polarization conditions and tend to rise consistently when the $I_{\rm ds}$ current flowing through the active channel is reduced (i.e. when $V_{\rm ds}$ and/or $V_{\rm gs}$ are lowered, with the former playing the major role). Consequently, for the devices under consideration, the definition of "contact resistances" is to be meant only as "differential" resistances, implying that the polarization conditions should be defined. It has to be stressed, however, that the contact resistances reported in this work are considerably lowered in comparison with the values observed in Ref. [29]. Indeed, the overall $R_{\rm C}$ values estimated at very low

 $V_{\rm ds}$ for PDIF-CN₂ are smaller by more than a factor 2× in comparison with those found for PDI8-CN₂ devices. Even larger improvements (up to a factor 5×) were found at increasing $V_{\rm ds}$, taking into account that, in this study, $R_{\rm C}$ at $V_{\rm ds}$ = 5 V was usually comprised between 5 and 10 K Ω cm.

Given these findings, it is evident that, in these systems, there is a relation of inverse proportionality between the estimated $R_{\rm C}$ and the mobility values. Indeed, while for PDI8-CN2 OTFT based on evaporated active channels maximum mobility values of 0.1 cm²/V·s are usually found, μ is increased by a factor of $6 \times$ when PDIF-CN₂ transistors fabricated with similar processes are considered. In the past, the relation $R_{\rm C} \propto 1/\mu$ was experimentally observed for several organic transistor configurations and was mainly explained within the theoretical framework of general diffusion-limited thermionic (DLT) phenomena as discussed by Malliaras et al. [42,43]. More recently, this model has been further investigated by Liu et al. [44], to describe the diffusionlimited injection mechanisms occurring for several organic compounds and related device architectures. Within this model, interfacial emission mechanisms are discussed in terms of net interfacial current resulting from the balance between unipolar recombination of charge carriers at the electrode interfaces and thermionic emission of charge in thermal hopping systems, i.e. organic semiconductors, mediated by the presence of a micrometric buffer region in the vicinity of the electrodes where majority carriers are depleted [45]. The resulting behaviour of the overall contact resistances as function of the applied V_{ds} voltage is described by an exponential function of the type:

$$Rc \approx A * \exp(-B \sqrt{V_{DS}}) + R_0 \tag{4}$$

where the fitting parameter A is strictly related to the effective energy barrier the interfaces ϕ_{eff} following the at relation Ax $(N_0\mu)^{-1} \exp(\varphi_{\rm eff}/kT)$. Here, N_0 indicates the average density of hopping sites in the organic semiconductors. Despite eq. (4) can be satisfactorily applied (not shown here) to shape the experimental $R_{\rm C}$ data found in this work, the contact resistance phenomenon affecting our devices cannot be explained by simply invoking the DLT model. Indeed, as demonstrated by the analysis of the SKPM profiles, the PDIF-CN₂ devices considered in this work are not just "injection-limited", since the values of R_s and R_d are at least comparable, with the latter becoming even dominant at low V_{ds} . This consideration is further



Fig. 6. (a) Calculated electric field profiles over the channel for different V_{ds} (0.2 V < V_{ds} < 5 V). The black patterned edges indicate the source (left) and drain (right) interfaces. The electric field *E* inside the active channel appears to flicker around average values of $E_{avg} = -0.14$, -0.07, -0.015, -10^{-4} Vµ⁻¹ for $V_{ds} = 5$, 3, 1, 0.2 V respectively. (b) Natural logarithm of the current vs. natural logarithm of the electric field in the access regions close the drain (circles) and source (squares) electrodes. These curves were deduced considering the maximum interfacial electric field values for different V_{ds} . The upper x-axis and the right y-axis indicate the corresponding electric field values expressed in V/m and the drain-source current values expressed in µA, respectively.

supported by the findings of the variable temperature experiments, demonstrating that $R_{\rm C}$ values follow a thermally activated behaviour with an activation energy being practically the same of the charge carrier mobility and, hence, lower than what predicted by the DLT models as discussed in Ref. [41] and in Ref. [26] where the energetic disorder is also taken into account.

Quite significantly, in fact, in this study we did not observe by AFM (see Fig. S4) remarkable differences (i.e. size and/or shape of the crystalline domains) in the film morphology for distances comparable with those identified in the SKPM profiles (i.e. several hundreds of nanometers).

In order to gain more insights about the physical reasons ruling the observed effects, we analyzed the local correlation between the electric field distribution along the channel in accumulation mode and the related I_{ds} current flowing across the device. The longitudinal electric field (*E*) across the entire active channel has been calculated by numerical derivation of the acquired surface voltage profiles, i.e. $E = -\frac{\partial V}{\partial x}$ (Fig. 6(a)) where *x* is the scanning direction along the channel.

In the regions close the electrodes, the electric field appears highly peaked and absolute values as high as 2 V μ m⁻¹ for *E* are observed to build up in proximity of the Source contact. Since the electrode/organic interfaces and the central channel are electrically in series and, hence, crossed by the same *I*_{ds} current, the *I*-*E* characteristics related to the source and the drain access regions were obtained (Fig. 6(b)). From the ln-ln plot of these data, we can notice that the currents do not exhibit a linear trend with *E*, but, rather, their slopes follow closer a quadratic behaviour (n = 2) which is commonly ascribed to Space-Charge-Limited Conduction (SCLC) mechanism [46].

Summarizing, in the OTFT accumulation mode, while the central channel behaves ideally displaying a quite linear dependence between the current and the existing electric field, the access regions near the gold electrodes are characterized by significantly inferior charge transport features. Hence, these two semi-insulating charge-depleted regions, connecting the electrodes and the central conducting channel, exhibit a non-linear electrical response. The presence of SCLC conduction mechanism [46] in the proximity of the source/drain electrodes could be invoked to justify the observed phenomenology. However, alternative explanation cannot be excluded and, in particular, the observed supralinear dependence of the injected and extracted current on the electric field, could be ascribed to the longitudinal electric field

dependence of charge carrier mobility in organic semiconductors, as reported by Silveira et al. [47]. Lastly, our experimental data suggest that, if present, the theoretically predicted energy barrier at the injecting electrode (\sim 0.5 eV) seems to have a minor effect respect to the observed injection and extraction mechanisms. Hence, the occurrence of a different energy alignment at the PDIF-CN₂/Gold interface, in which the role of energetic disorder is dominant, should be hypothesized [26,29].

5. Conclusions

In conclusion, in this work, the contact resistance $(R_{\rm C})$ effects in PDIF-CN₂-based bottom-contact bottom-gate OTFTs have been evaluated by means of Scanning Kelvin Probe Microscopy (SKPM). In agreement with previous results achieved for a parent perylene diimide compound, the performed experiments reveal that, in these devices, contact resistance ($R_{\rm C}$) values decrease when the $I_{\rm ds}$ current flowing across the devices is increased. The effect of the applied V_{ds} voltage is, in particular, quite remarkable. The careful inspection of the surface voltage profiles acquired during the device operation allowed us to evaluate also the charge carrier mobility excluding any contact-related contribution. Interestingly, the contact resistance at the drain electrode was found to dominate for longitudinal electric fields approaching to zero, contradicting the most direct theoretical picture according to which the operation of these devices is purely injection-limited at the source contact. As a general result of this investigation, contact effects appear to be ascribable basically to the electronic properties of the organic film regions (extending for several hundreds of nanometers) in the proximity of the gold electrodes. Apparently, the discussed experimental data cannot be justified only in terms of morphological arguments, being more compatible with a scenario where energetic disorder, space-charge-limited conduction phenomena and the longitudinal electric field dependence of charge carrier mobility could provide the most significant contribution to the R_C phenomenon in the low $V_{\rm ds}$ region.

Notes

The authors declare no competing financial interest.

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Appendix A. Supplementary data

Supplementary data related to this article can be found at http://dx. doi.org/10.1016/j.orgel.2017.10.021.

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